INDIAN INSTITUTE OF INFORMATION TECHNOLOGY DESIGN AND MANUFACTURING, KANCHEEPURAM

भारतीय सूचना प्रौद्योगिकी,
अभिकल्पना एवंवि निर्मानसंस्थान,   कांचीपुरम

(An Institution of National Importance under MHRD, Government of India)
Melakottaiyur, Off Vandalur-Kelambakkam Road, Chennai-600127

Ph: +91 44 2747 6313   Fax: +91 44 2747 6301   Email: sricce@iiitdm.ac.in

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Advertisement for LAB ENGINEER Positions in Sponsored Project

Indian Institute of Information Technology Design and Manufacturing, Kancheepuram, Chennai is an Institution of National Importance established in 2007 by the Ministry of Human Resource Development, Government of India and is located at outskirts of Chennai. The institute is at a distance of 25 KM from Chennai international airport, 15 KM from Tambaram railway station and 40KM from Chennai central. It is a Centre of Excellence for pursuing Design and Manufacturing oriented Engineering Education and Research for promoting competitive advantage of Indian products in global markets.

IIITDM Kancheepuram, Chennai is presently offering undergraduate, postgraduate, dual-degree programmes in the areas of Computer, Electronics, Mechanical Engineering and inter-disciplinary Ph.D programmes in core and applied areas of basic sciences and engineering. It functions with the vision to groom engineers with design and manufacturing skills. The institute gives significant amount of emphasis for practice courses and theory concepts are also explored along with the relevant laboratory course.IIITDM Kancheepuram invites online application for the following project positions:

General Conditions/Instructions for all posts:

Name of the Research Project	**Special Manpower Development Programme for Chips-to-System Design (SMDP-C2SD Project)**
Duration of the project	**18** Months
Name of the Principal Investigator / Co-P.I	Dr. M Noor Mahammed SK, Dept. of Computer Science / Dr. Binsu J Kailath, Dept. of ECE
Funding Agency	MeitY, Govt. Of India
Name of the Project Position	LAB ENGINEER
Number of Positions	One (**1 Position**)
Qualification	**Essential Qualification** B.Tech / B.E (ECE) / M.E / M.Tech degree in VLSI Design with minimum 6.5 CGPA(or 60% marks) **Desirable qualification** Experience in VLSI / Related area, working knowledge in VLSI tools like Cadence / Synopsys / Mentor / Xilinx, etc., publication in reputed Journals
Age	Below 30 Years
Stipend	**Rs. 34,000/- Per Month** (Stipend will be up to duration of the project i.e., <u>18 months</u>)
Other Important Point	The selected candidates will be eligible to register for the **Ph.D degree** at IIITDM Kancheepuram, Chennai as per the eligibility criteria against subsequent advertisement (*possibly in July 2019*).

1. Only Indian Nationals need to apply.

2. The candidates are required to work for the success of the project.

3. Candidates should bring self-attested copies of the relevant certificates/testimonials along with the original certificates/documents at the time of interview.

4. No travelling or any other allowances is admissible for attending the interview.

5. The Institute reserves the right not to fill up the position, if it so decides.

6. The Institute reserves the right to terminate the appointment at any time before completion of the period, if it so decides.

7. No interim correspondence will be entertained.

8. Canvassing in any form will lead to disqualification.

9. Last date for submission of online application is **16thApril 2019.**

Test: Candidates have to appear for a written test of one hour duration followed by personal interview. The syllabus for both written and oral is signals & systems, digital signal processing and advanced digital signal processing.

Last date for submission of online application:**16thApril 2019**

Tentative date for written test and interview on: **29thApril 2019**

Place of Interview:

Indian Institute of Information Technology Design and Manufacturing, Kancheepuram, Chennai
(an institute of national importance, Fully funded by Govt. of India)
Melakkottaiyur, Off Vandalur-Kelambakkam Road,
Chennai-600127
Contact No: 044-27476313
Email: sricce@iiitdm.ac.in

Sd/x

Dean (SR)

IIITDM Kancheepuram