

NATIONAL INSTITUTE OF TECHNOLOGY ANDHRA PRADESH

SRI VASAVI ENGINEERING COLLEGE CAMPUS, PEDATADEPALLI TADEPALLIGUDEM– 534102, WEST GODAVARI DIST., ANDHRA PRADESH, INDIA. Department of Electrical Engineering

#### No. NITAndhra/DEE/TR/SERB-EEQ/JRF/2019

Date: 25.06.2019

# Advertisement for the Position of JRF

Applications are invited from the interested candidates for one temporary position of **Junior Research Fellow (JRF)** under Department of Science and Technology (DST) - Science & Engineering Research Board (SERB), Empowerment and Equity Opportunities for Excellence in Science Award, Govt. of India, sponsored project entitled **"Development of Five-Level Inverter Fed Direct Torque and Flux Controlled Sensorless Permanent Magnet Synchronous Motor Drive for Torque Ripple Minimization in Hybrid Electric Vehicle"**, The project duration is 3years.

### Name of the Position & Number of Posts:

Junior Research Fellow (JRF) & 01-post.

### **Essential Qualification:**

First class with not less than 60% aggregate (or) not less than 6.5 CGPA on 10-point scale in B.E/B.Tech. in Electrical Engineering/Electrical and Electronics Engineering.

and

First class with not less than 60% aggregate (or) not less than 6.5 CGPA on 10-point scale in M.E /M.Tech. in the specialization of Electrical Drives and Control/Power Electronics/Power Electronics and Drives/Industrial Drives and Control/Control System. The candidate must have M.Tech. through GATE (or) a valid GATE score (or) GATE Qualification.

#### **Desirable Qualification:**

Analytical and experimental activities during M.E/M.Tech. for the development in power electronics based system with concluding understanding, knowledge and experience with technical software/tools (MATLAB/SIMULINK, PSIM, SNSYS, etc) for simulation studies and dSPACE/FPGA/Micro/DSP controllers for the implementation of control techniques.

#### Fellowship (Consolidated):

**Rs.** ₹31,000/- pm for 2 years and ₹35,000/- pm for the  $3^{rd}$  year. House Rent Allowance (HRA) as per Institute rules.

### How to Apply:

Interested and eligible candidates are required to apply in the prescribed application format available on the institute website <u>www.nitandhra.ac.in</u>. Complete application with the supporting copies of certificates should be sent as hard copy by post to the address mention below with a superscription on the envelope APLLICATION FOR THE POST OF JUNIOR RESEARCH FELLOW (SERB-EEQ) and a softcopy of application with supporting documents should be sent by email to <u>tramesh.ee@nitandhra.ac.in</u> so as to reach by 19.07.2019. Shortlisted candidates will be intimated through email/institute website by 22.07.2019. Shortlisted candidates will have to report for written

test and/or interview on 26.07.2019 (Tentative) at 9:30AM in the Department of Electrical Engineering, NIT Andhra Pradesh.

#### Selection Procedure:

The candidates will be selected through written test and/or interview.

## Note:

- Candidates who are already employed should produce No Objection Certificate (NOC) from the employer and students should provide provisional degree certificate or its equivalent at the time of interview. The selected candidate is expected to produce a Relieving Certificate from their employer or provisional degree certificate in case of students and join the duties within a week after intimation of their selection else it would be offered to a candidate in waiting list.
- 2. No TA/DA will be paid for attending the interview.
- 3. The selected candidate is eligible to register for PhD degree at NIT Andhra Pradesh as per institute rules.
- 4. Decision of Director NIT Andhra Pradesh will be the final in all respect.

## ADDRESS FOR CORRESPONDENCE: PI

Dr. Tejavathu Ramesh Assistant Professor Department of Electrical Engineering National Institute of Technology, Andhra Pradesh Tadepalligudem – 534 102, West Godavari Dist., Andhra Pradesh, India. Email: tramesh.ee@nitandhra.ac.in Phone. No: 9034003377.



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SRI VASAVI ENGINEERING COLLEGE CAMPUS, PEDATADEPALLI TADEPALLIGUDEM– 534102, WEST GODAVARI DIST., ANDHRA PRADESH, INDIA. Department of Electrical Engineering

#### **Application for the Position of JRF**

### DST-SERB Sponsored Project:

"Development of Five-Level Inverter Fed Direct Torque and Flux Controlled Sensorless Permanent Magnet Synchronous Motor Drive for Torque Ripple Minimization in Hybrid Electric Vehicle".

## Last Date of Application: <u>19.07.2019</u>

1.	Name of the Candidate (In BLOCK LETTERS):						
		Passport size					
2.	Date of Birth (DOB):	Recent Photograph					
3.	Sex (M/F):						
4.	Marital Status (Single/Married):						
5.	Category (General/OBC/SC/ST):						
6.	Nationality:						
7.	Name of the Post applied for:						
8.	Phone No.:						
9.	Mobile No.:						
10.	Email Address:						
11. Address:							
	Correspondence:						
	Permanent:						

12. Academic/Professional Qualifications (Attach the copy of proof, mark sheet/grade card, etc):

Educational Qualifications	Institute/ University/ Board	Discipline/ Field	Year of Passing	Class/ Division	% of Marks/ CGPA
M. Tech./M.E					
B. Tech./B.E					
12 <sup>th</sup> Standard					
SSC/ 10th					

13. GATE/NET Qualification (Yes/No):

Appeared Year:

GATE/NET Rank:

GATE/NET Score:

14. Details of work Experience if any: (Enclose the copy of experience certificate and/or NOC)

15. Previous Research work and Publications if any (enclose first page of publication):

#### **DECLARATION**

I \_\_\_\_\_\_ hereby declare that all the statements made in this application are true, complete and correct to the best of my knowledge and belief. In the event of the information being found false or incorrect or any ineligibility being detected before or after the selection, my candidature is liable to be cancelled.

Date:

Signature of the Applicant

Place: