



Department of Electrical and Electronics Engineering

National Institute of Technology - Tiruchirappalli

Tanjore Main Road, National Highway 67, Near BHEL Trichy, Tiruchirappalli, Tamil Nadu 620015

Advertisement No. _____

Date: 18/06/2019

Call for the position of Junior Research Fellow (JRF) for the CPRI Project on
“Design, Implementation and Analysis of Wireless Power Transfer and PV
System for Battery Charging of Passenger e-Bus”

Applications are invited from motivated and eligible candidates for the position of Junior Research Fellow (JRF) for the *Central Power Research Institute (CPRI), Govt. of India, Bangalore* funded research and development Project on “Design, Implementation and Analysis of Wireless Power Transfer and PV System for Battery Charging of Passenger e-Bus”.

Essential Qualification

M.Tech./M.E. (*with GATE qualification*) in Power Electronics and Drives/ Power System/Electric Vehicles/ Energy Systems/ Renewable Systems with thesis in the area of power electronics based system.

Desirable Qualification

Analytical and experimental activities during M. Tech for the development in power electronics based system with concluding understanding, knowledge and experience with technical software/tools (COMSOL, ANSYS, PSIM, MATLAB/SIMULINK) for simulation studies and micro controllers /FPGA controllers for the implementation of control techniques.

Fellowship

Rs. 31,000p.m. + HRA@ 20% (Two years)

Duration

2 Years-At the beginning assignment will be for one year, which can later be extended every six months up to completion of project.

How to apply

Interested candidates are requested to submit application in proper format (available online on institute website) and detailed curriculum vitae through email and hard copy of application should reach the Principal Investigator (PI), by July 17, 2019.

Shortlisted candidates will be intimated through email by July 19, 2019.

Shortlisted candidates will have to report for written test/interview on July 26, 2019 (tentative) (10:30AM) in Department of Electrical and Electronics Engineering, NIT Trichy.

Note

1. No TA/DA will be paid for appearing written test/interview.
2. Candidate selected for this position can also apply for the regular PhD program provided they satisfy the eligibility criteria for PhD program.
3. Decision of Director NIT Trichy will be the final in all respect.

Correspondence address: PI

Dr. P Srinivasa Rao Nayak,

Assistant Professor

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RECRUITMENT OF TEMPORARY PROJECT STAFF -- APPLICATION FOR THE POST OF JRF

For Office Use only	Paste here your recent passport size colour Photograph
Serial Number allotted to the Applicant	
Signature of the Application Screening Committee Chairman	

APPLICATION FOR THE POST OF RESEARCH JRF

1. Name of the Candidate in full					
2. Nationality					
3. Mother Tongue					
4. Date of Birth	Date	Month	Year		
5. Sex	Male / Female				
6. Marital Status	Married / Single				
7. Category SC / ST / OBC / General	Please specify				
8. Address for communication with Mobile number and E Mail ID					
9. Project Title					
10. Name of the Post applied for					
11. Advertisement Number with Date					
Academic Career: Enclose photo copies of the qualifying degree certificates and mark sheets/ grade cards showing the percentage of marks or CGPF from HSC onwards					
Name of Exam	School / University	Board/ University	Broad subjects of study	Year of Passing	% of marks / GCPA
S.S.L.C					
H.S.C					
Graduation					
Post-Graduation					
Any other					

Enclose self-attested Xerox copies

12. GATE ScoreSubject
.....Year.....

13. NET Score
.....Subject.....Year.....

14. Work Experience, if any

Organization	Designation	Duration		Nature of Job
		From	To	

15. Previous Research work and publication, if any (enclose copies of published or unpublished work)

16. Proof of past study as a sponsored candidate, or other contractual obligation, if any:

17. Reference

Name	Address	Phone & E Mail ID

18. I do not have a relative among Faculty and Staff of the NIT, Tiruchy.

19. Declaration: I do hereby that the information furnished in the application is true to the best of my knowledge and belief. If admitted, I shall abide by the rules and regulations of the Institute and Project associated to it. If any information furnished in this application is found to be untrue, I am liable to forfeit the seat / appointment allotted to me any time in future and legal action be taken against me.

Date:

Full signature of the Applicant

List of Enclosures: (1)

(2)

(3)

(4)